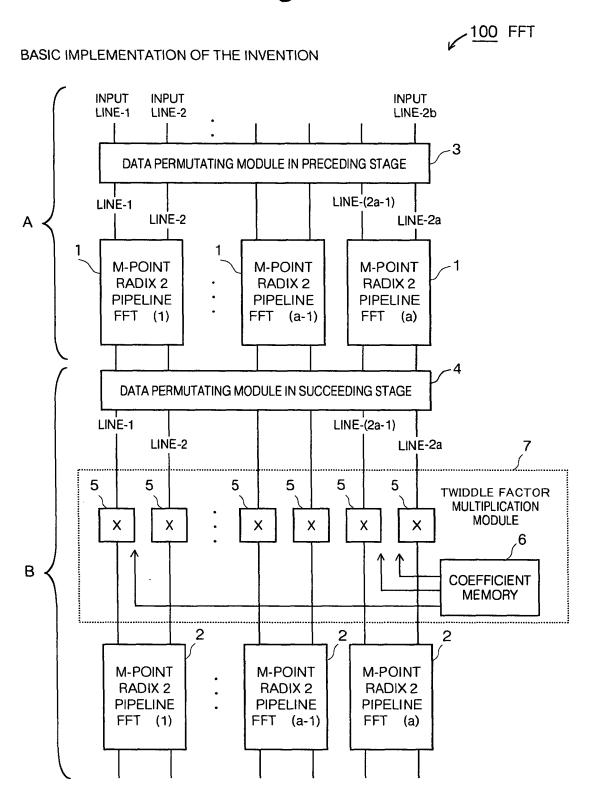
INVENTORS: Toshiro NAKAZURU et al. SERIAL NO.: To be Assigned DOCKET NO.: 1602.1027

Fig. 1



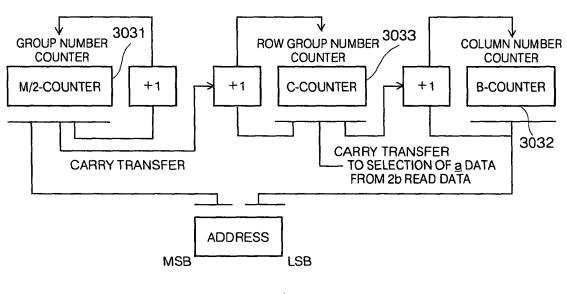
INVENTORS: Toshiro NAKAZURU et al. SERIAL NO.: To be Assigned DOCKET NO.: 1602.1027

Fig. 2

DATA PERMUTATING MODULE IN PRECEDING STAGE (METHOD 1) 302 WRITE ADDRESS **GENERATING CIRCUIT** 301 PARALLEL INPUT OF 2b DATA SELECT a DATA FROM 2b DATA INPUT OF a DATA TO ONE INPUTS OF INDIVIDUAL PIPELINE FFTs **BUFFER MEMORY BUFFER MEMORY** INPUT OF a DATA TO OTHER (BANK-A) (BANK-B) INPUTS OF INDIVIDUAL PIPELINE FFTs 301A SELECT a DATA FROM 2b DATA 301B 303 FIRST READ ADDRESS **GENERATING CIRCUIT**

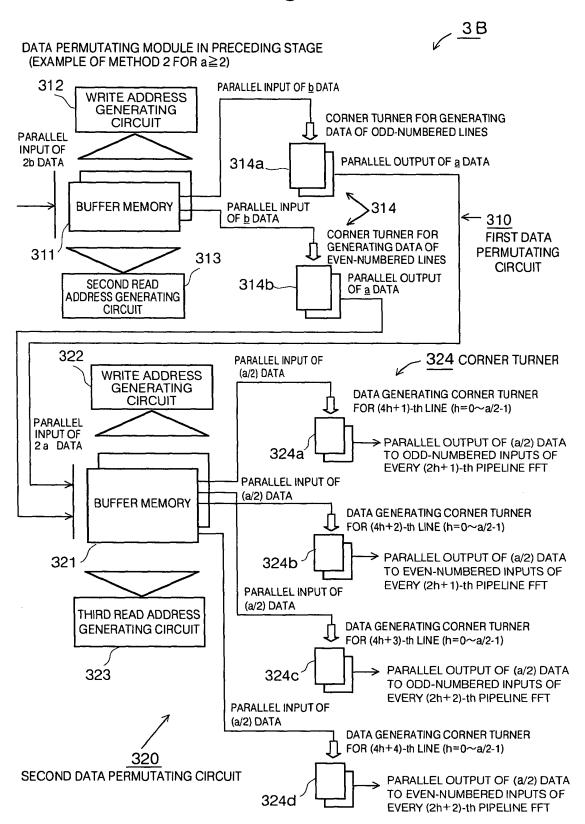
Fig. 3

FIRST READ ADDRESS GENERATING CIRCUIT



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Fig. 4



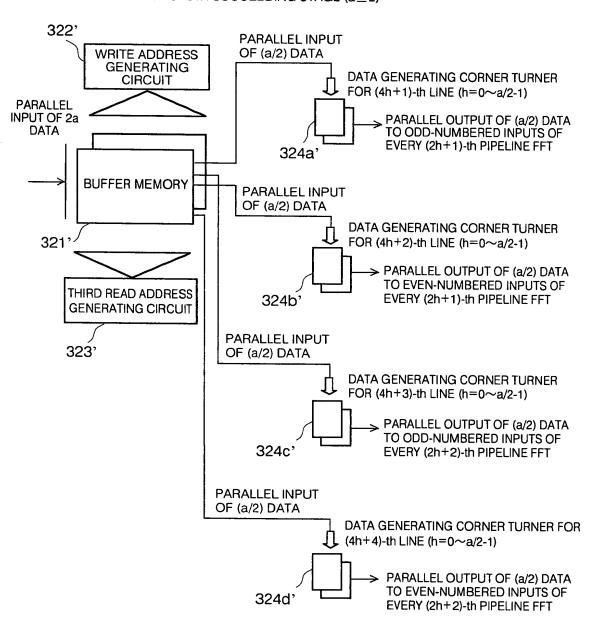
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Fig. 5

4 A

DATA PERMUTATING MODULE IN SUCCEEDING STAGE (a≥2)



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Fig. 6

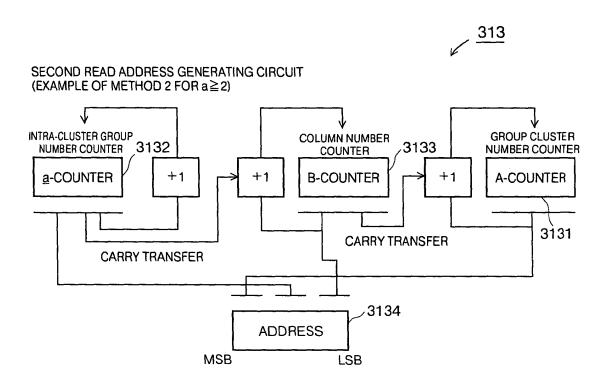
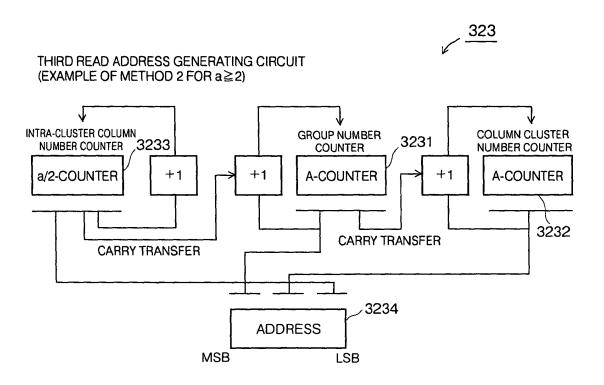


Fig. 7

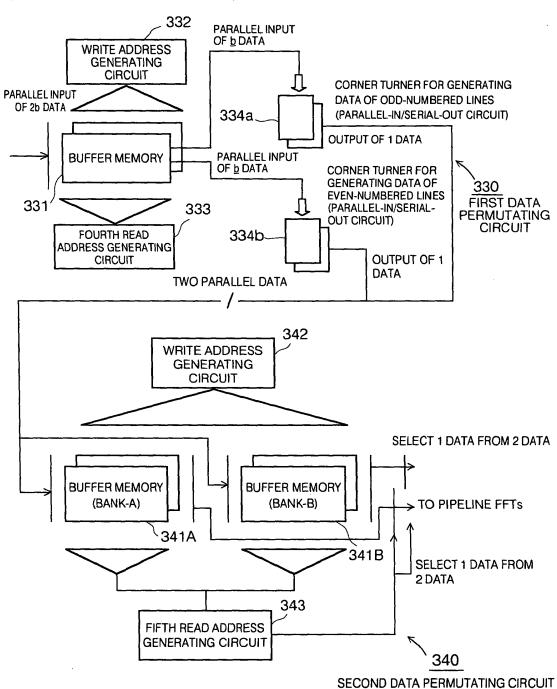


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Fig. 8

<u>3C</u>

DATA PERMUTATING MODULE IN PRECEDING STAGE (EXAMPLE OF METHOD 2 FOR a=1)



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Fig. 9

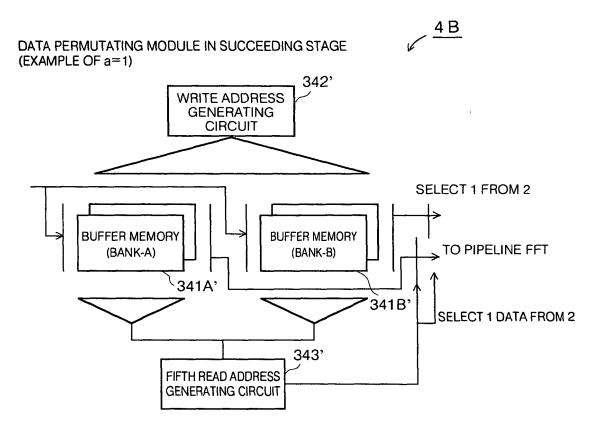
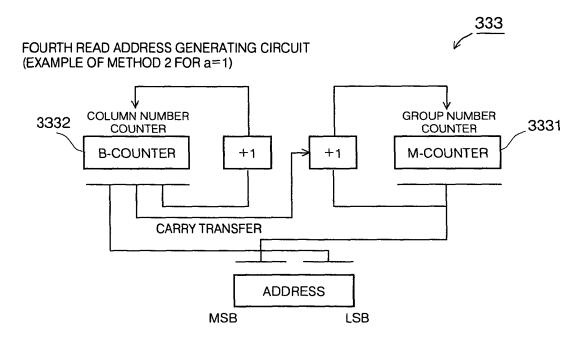
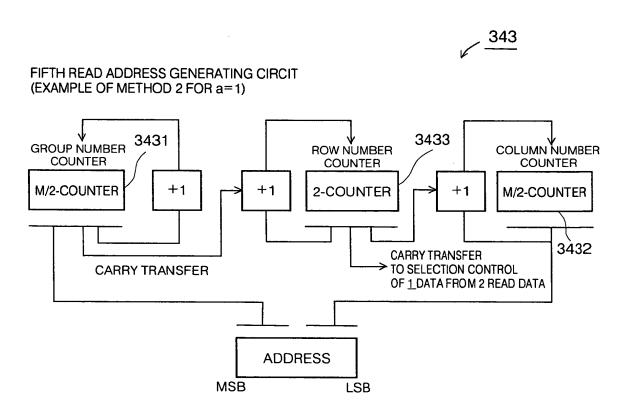


Fig.10



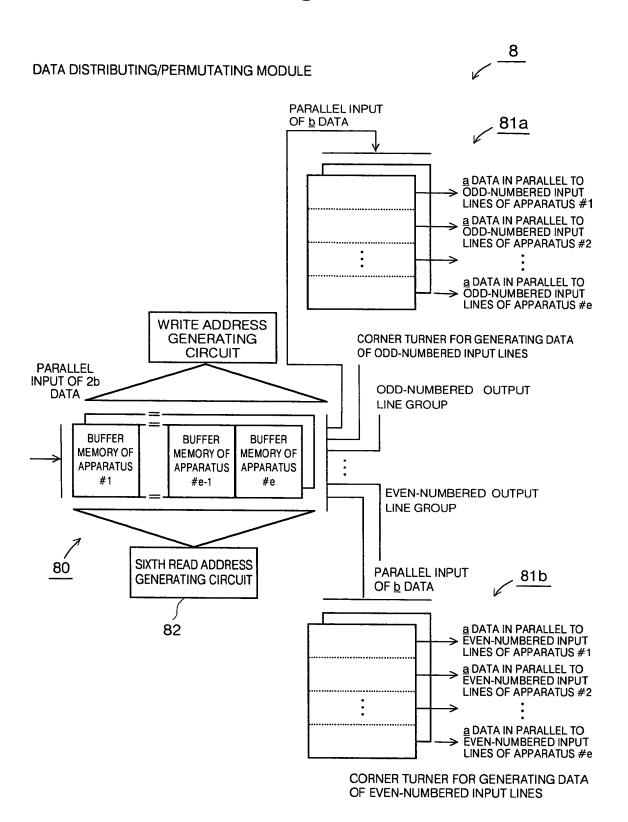
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Fig.11



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Fig.12



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Fig.13

SIXTH READ ADDRESS GENERATING CIRCUIT (EXAMPLE FOR a ≥ 2)

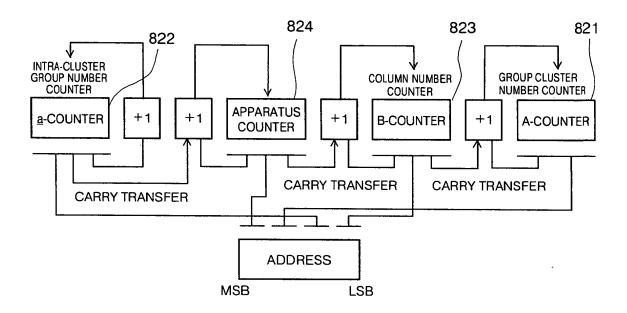
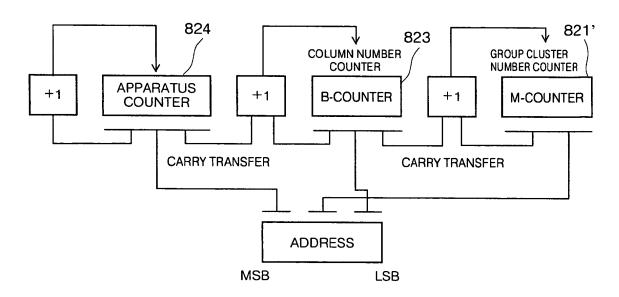


Fig.14

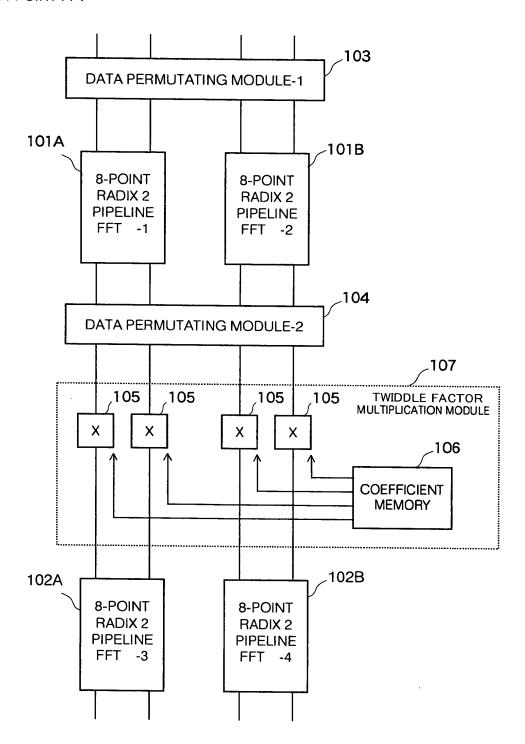
SIXTH READ ADDRESS GENERATING CIRCUIT (EXAMPLE OF a=1)



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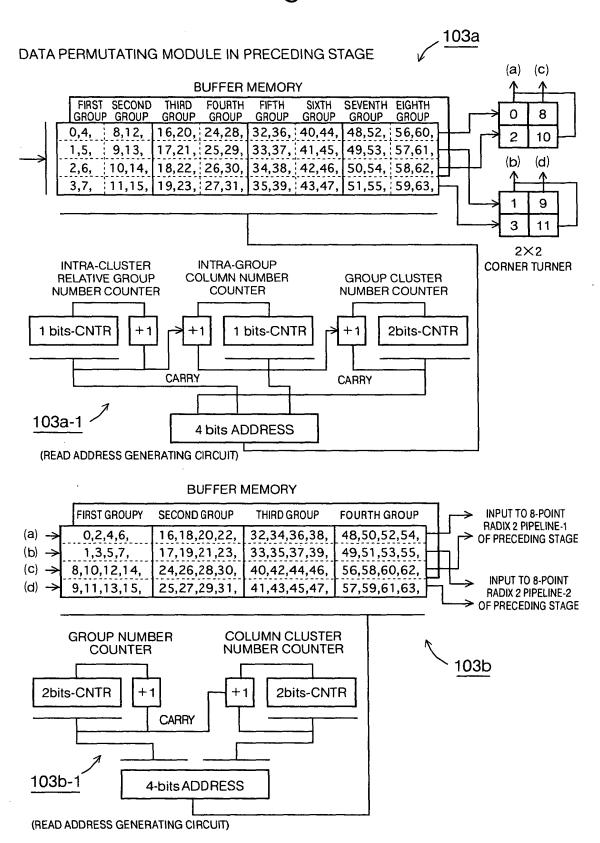
Fig.15

64-POINT FFT



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Fig.16



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Fig.17

